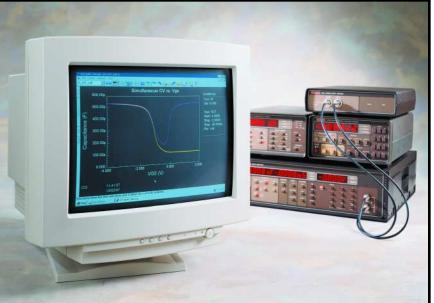
# System

## Simultaneous C-V Systems





- Ultra-thin gate oxide characterization down to 30Å
- Oxide charge characterization, such as mobile ion, interface trap charges
- MIS device characterization, including threshold voltage, oxide thickness, etc.
- **Carrier lifetime and generation** velocity
- Accurate doping profile with C-V
- Industry's only simultaneous C-V and quasistatic C-V with feedback charge technique
- Easy-to-use test development software
- C-V only systems employ Metrics-ICS software
- C-V/I-V combination systems may employ either Keithley's Model 4200-SCS Semiconductor Characterization System as the controller or the Metrics-**ICS** software

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### market and maintain device vields. Our solutions are used around the globe for applications that include: Developing and integrating new semiconductor processes

systems.

· Research and development of new materials and device structures, such as thin oxides, high- $\kappa$ , and low- $\kappa$  dielectrics

Keithley supplies C-V products for virtually every stage of semiconductor research, development,

and production processes. Our line of hardware and software products offers solutions for a broad array of measurements, including quasistatic and high frequency C-V. These can be measured either simultaneously or sequentially with Keithley C-V

Keithley C-V software provides comprehensive analysis of measured and calculated parameters to help semiconductor fabs shorten their time-to-

- · Post-metalization process characterization
- Device reliability
- · Analyzing failure mechanisms

### Simplified Approach to C-V Measurements

Keithley's C-V instruments using either Metrics-ICS software or controlled by the Model 4200-SCS Semiconductor Characterization System simplify characterizing devices accurately. For example, the System 82-WIN provides simultaneous C-V testing – quasistatic and high frequency C-V measurements

### **OPTIONAL SYSTEM ENHANCEMENTS**

I-V CAPABILITY		SWITCHIN	NG
4200-SCS	Semiconductor Characterization System	707A	Switching Matrix – up to 8×72
236	Source-Measure Unit	708A	Switching Matrix - up to 8×12
237	High Voltage Source-Measure Unit	7174A	8×12 Low Current Matrix Card
238	High Current Source-Measure Unit	7072A	8×12 Semiconductor Matrix Card
2400	SourceMeter		
2410	High Voltage SourceMeter		
2420	High Current SourceMeter		
2430	Pulse SourceMeter		

### **ACCESSORIES AVAILABLE**

RANGE EXTENDERS		
5904	Range Extender for 20nF range on 590	

- RACK MOUNT KITS Dual Fixed Rack Mount Kit (for 230-1 and 595) 1019A-2
- 2288 Fixed Rack Mount Kit (for 590)

MISCELLANEOUS

5905, 5906, 5907, 5909, and 5955 Calibration and Verification Sources

IEEE-488 INTERFACE		
KPC-488.2AT	Interface Card	
KPCI-488.2	Interface Card	
SOFTWARE		
Metrics-ICS	Windows-based Interactive Characterization Software including drivers for C-V and I-V instruments (included with Model 82-WIN)	
Metrics-ICS-CV	C-V Analysis Libraries for Metrics-ICS (included with Model 82-WIN)	
Metrics-ICS-SW	Switch Drivers for Metrics-ICS	



### System 82-WIN

### **Ordering Information**

### System 82-WIN

Factory-integrated, rack mounted, and assembled Simultaneous C-V System for Windows. Includes Model 230-1 Programmable Voltage Source, Model 595 Quasistatic C-V Meter, Model 590 100k/1M C-V Analyzer, Model 5901 100k/1M C-V Analyzer, Model 5951 Remote Input Coupler, Model 5909 Calibration Sources, Metrics-ICS software, Metrics-ICS-CV analysis libraries, computer and monitor (contact factory for complete configuration).

### Model 82-WIN

Similar hardware to System 82-WIN, but without computer, monitor or rack-mounting hardware. Requires customersupplied Windows-based computer. Customer provides system integration.

### System 83

Factory integrated, rack mounted, and assembled Simultaneous C-V System including computer and monitor plus prober and hot chuck (contact factory for complete configuration).

### System 86-WIN

Factory integrated, rack mounted, and assembled Simultaneous C-V System plus I-V and switching capability, including computer and monitor (contact factory for complete configuration).

These products are available with an Extended Warranty.

### **Accessories Supplied**

See system descriptions in Ordering Information.

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## Simultaneous C-V Systems

can be performed in a single voltage sweep. This simultaneous technique, which is unique to Keithley, improves C-V measurement accuracy. This technique eliminates the need for multiple measurements, theoretical curves, and doping profile assumptions.

Simultaneous C-V testing with a fully integrated system including I-V measurements offers a variety of additional benefits. The simultaneous method eliminates errors due to changes in device characteristics between the first and second sweeps, which are caused by voltage stress. It also eliminates curve alignment errors due to a variety of factors. This solution minimizes hardware and software integration problems, while the automated test sequences this approach allows also help increase productivity.

Keithley's System 82-WIN includes parameter extraction algorithms for C-V and C-t measurement. Table 1 lists some of the parameters calculated by these libraries. Device parameters such as interface trapped charge density, mobile charge density, doping profile, generation lifetime, threshold voltage, etc. are available and displayed immediately after measurement.

### From Individual Instruments to Fully Integrated Systems

Keithley supplies a comprehensive range of instruments, software, and integrated systems that allow MOS semiconductor fabs to choose the best fit for their application, whether it's basic technology research, device development, or process characterization. Table 1 offers an overview of Keithley's C-V product features and measurement capabilities.

### Table 1. Keithley C-V Product Capabilities

Table 1. Keithley C-V Produ	ci oquadinnes	82-WIN Systems <sup>1,2</sup>	590 C-V Analyzer w/Metrics-ICS or 4200-SCS
Oxide Charge Characterization	Interface Trap Density, Band Bending	•	
	Mobile Ion Tests: BTS Method STVS Method	•	•
	Effective Oxide Charge Density, Q <sub>EFF</sub>	•	•
MIS Device Parameters	Oxide Thickness	•	•
	Series Resistance	•	•
	Flatband C and V	•	•
	Threshold Voltage	•	•
	Bulk Doping	•	•
	Metal-Semiconductor Work Function	•	•
	Debye Length	•	•
	Bulk Potential	•	•
	Average Doping	•	•
Doping Profiles	Interface Trap Correction	٠	
	Junction Doping	•	
	Pulsed High-Frequency C-V	•	•
Lifetime C-t	Capacitance vs. Time	•	•
	Zerbst Plot	•	•
	Minority Carrier Lifetime	•	•
	Surface Generation Velocity	•	•
Miscellaneous	Quasistatic C-V and Q/t vs. V	•	
	High Frequency C-V and G or R vs. V	•	•
	Simultaneous C-V	•	
	Automatic Test Sequences with Switch Control	ol •	
	Thermal Chuck Control	•	

1. Model 82-WIN is an instrument cluster, plus Metrics-ICS software for a Windows-based PC. The PC and system integration are supplied by the user. However, the embedded computer in the Keithley 4200-SCS can be used as the controller.

However, the embedded computer in the keithley 4200-SCs can be used as the controller. 2. System 82-WIN is a factory integrated C-V characterization system customized to user specifications, supplied with a Pentium computer and Windows operating system.

The System 86 is a factory integrated C-V and I-V characterization system customized to the user's specifications. It includes a custom-configured Model 4200-SCS Semiconductor Characterization System to provide from two to eight SMUs for combined I-V and C-V measurements—all controlled via the Keithley Interactive Test Environment software included in the Model 4200-SCS.



SEMICONDUCTOR

### System 82-WIN

### **Ordering Information**

### Metrics-ICS Software METRICS-ICS-35

Interactive Characterization Software (Version 3.5.2) core program, includes I-V and C-V instrument drivers. Software supplied on CD-ROM.

METRICS-ICS-35C-V

C-V Analysis Libraries for Models 590, 595, and 82. Supplied on 31⁄2″ disks.

### **METRICS-ICS-35SW**

Keithley Switch Drivers (Requires version 3.5; specify METRICS-ICS serial number if ordering separately)

### Upgrades

### MET-UP-35

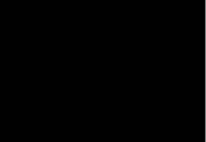
Simultaneous quasistatic and high frequency C-V measurements

Upgrade for any version of Metrics-ICS to version 3.5.2. Includes C-V and I-V drivers.

### MET-UP-SW-35

Upgrade for any version of Metrics-ICS to version 3.5.2. Includes C-V and I-V drivers PLUS switch drivers. (User must already own the switch drivers.)

When ordering Metrics-ICS upgrades, always include the METRICS key number (same as serial number), drivers owned, and user name, company name, address, and phone number.



## Simultaneous C-V Systems

### Metrics-ICS Software

### Flexible and User Friendly Test Software

Metrics-ICS software permits easy control of a variety of Keithley I-V and C-V instruments and switching systems for complete semiconductor characterization capability. Measure and display device characteristics, such as current vs. voltage (I-V) curves, leakage current, high frequency and low frequency (quasistatic) C-V curves.

### Point, Click, Measure

Display data automatically in a tabular format using Metrics-ICS's DataView spreadsheet or graphically using the package's custom PlotView tools. The Measurement Remote Control makes it simple to execute a variety of measurement modes, such as single, append, repeat, stress vs. time, sample vs. time, and auto-sequence measurements.

### **Process Development**

Automate parameter extraction, such as Threshold Voltage, using the Numerical Transform Editor. The Transform Editor defines nested equations used to extract parameters from measured data.

### **Device Characterization**

Automatically characterize new devices using auto-sequence execution of measurements. Data is stored based on user-defined attributes, such as process, lot, wafer, die, and more.

### **Process Monitoring/Reliability**

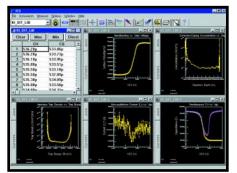
Solve in-line production problems by tracking parameters such as line width, resistivity, and mask alignment. Automatically export results to generate early warning reports. Perform on-wafer reliability tests, such as Metal Migration, Time Dependent Dielectric Breakdown, and Hot Carrier Injection.

### **Metrics-ICS Specifications**

#### **INSTRUMENTS SUPPORTED:**

IV Instruments: 213, 236, 237, 238, 2400, 2410, 2420, 2430 (two or more SMUs require Model 2361 Trigger Controller). CV Instruments: 590, 595, 82.

METRICS-ICS-SW Drivers: 707A, 708A, 7001, 7002 Mainframes; 7070, 7071, 7071-4, 7072, 7072- HV 7073, 7074, 7172, 7174A for the 707A/708A; 7011, 7052, 7152, 7153 for the 7001/7002.



The Keithley C-V Analysis Library, included with the Model 4200-SCS or the Metrics-ICS-35CV, shows interface trap density and other extracted MIS device parameters.

### GENERAL

#### SYSTEM REQUIREMENTS:

Minimum: 233MHz Pentium, 32MB RAM for Windows 95/98, 64MB RAM for Windows NT 4.0 or 2000, 6GB hard drive, Windows 95, 98, NT 4.0 with Service Pack 6, or 2000, Centronics parallel port.

Suggested: 350MHz PentiumII, 64MB RAM for Windows 95/98, 128MB RAM for Windows NT 4.0 or 2000, 6GB hard drive, Windows 95, 98, NT 4.0 with Service Pack 6, or 2000, Centronics parallel port. SUPPORTED INTERFACES:

Keithley: KPC-488.2, KPC-488.2AT, KPCI-488.



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### System 82-WIN

## Simultaneous C-V Systems

### **HIGH FREQUENCY CAPACITANCE**

100 kHz:		ACCURACY (1 Year, 18°–28°C)	TEMPERATURE COEFFICIENT (0°–18°C & 28°C–40°C)	NOISE
RANGE	RESOLUTION	$\pm (\% rdg + pF)$	(0 −18 C & 28 C−40 C) ±(%rdg)/°C	P-P
2 pF	0.1 fF	1.0 + 0.01	0.03	10 fF
20 pF	1 fF	0.7 + 0.01	0.03	20 fF
200 pF	10 fF	0.7 + 0.05	0.03	180 fF
2 nF	100 fF	0.9 + 0.5	0.08	1800 fF
20 nF <sup>2</sup>	1 pF	1.4 + 10	0.3	18 pF

1 MHz: range	RESOLUTION	TEMPERATURE ACCURACY (1 Year, $18^\circ - 28^\circ$ C) $\pm (\%rdg + pF)$	COEFFICIENT (0°-18°C & 28°C-40°C) ±(%rdg)/°C	NOISE P-P
20 pF	1 pF	0.9 + 0.02	0.03	20 fF
200 pF	10 fF	0.9 + 0.05	0.03	200 fF
2 nF	100 fF	1.4 + 0.5	0.14	400 fF

SHUNT CAPACITANCE LOADING EFFECT: 0.1% of reading additional error per 100pF load with equal shunt load on input and output.

TEST VOLTAGE: 15mV rms ± 10%.

TEST FREQUENCY TOLERANCE: ±0.1%.

### QUASISTATIC CAPACITANCE<sup>3</sup>

100 kHz:

RANGE	RESOLUTION	ACCURACY (1 Year, 18°–28°C) ±(%rdg + pF)	NOISE P-P (Typical)
200 pF	10 fF	1.0 + 0.1	(0.12% rdg + 0.13 pF) × (100 mV/STEP V) + 0.01 pF
2 nF	100 fF	0.8 + 0.2	(0.09% rdg + 0.13 pF) × (100 mV/STEP V) + 0.1 pF
20 nF	1 fF	0.6 + 2.0	$(0.09\% \text{ rdg} + 0.13 \text{ pF}) \times$ (100  mV/STEP V) + 0.1  pF

TEMPERATURE COEFFICIENT (0°-18° & 28°-40°C): ±(0.02% rdg + 0.1pF)/°C.

#### NOTES:

1. Specifications are based on parallel RC model and Quality Factor ≥20. Assumes proper cable correction and open circuit suppression. 2. Requires 5904 range extending adapter.

3. Quasistatic capacitance accuracy is exclusive of noise, for STEP V ≥ 0.05V and DELAY TIME ≤ 1 second. For other parameters, derate by (5mV/STEP V) × (DELAY TIME/1 second) in pF at 23°C. Double the derating for every 10°C rise in ambient temperature above 23°C. Typical allowable non-equilibrium current plus leakage current: <20pA on 200pF range; <200pA on 2nF range during capacitance</p> easurements

### **VOLTAGE MEASUREMENT**

ACCURACY (1 Year, 18°-28°C): ±(0.05% rdg + 50mV). RESOLUTION: 10mV

TEMPERATURE COEFFICIENT (0°-18° & 28°-40°C): ±(0.005% + 1mV)/°C

### **VOLTAGE SOURCE**

P-P NOISE <sup>1</sup>			
VOLTAGE	(0.1 Hz to 10 Hz)	RESOLUTION	
$\leq 20 \text{ V}$	150 µV	10 mV	
> 20 V to 120 V	250 µV	100 mV	

Typically 3mV up to 75MHz

MAXIMUM SWEEP SPAN, VSTART - VSTOP : 40V

MAXIMUM OUTPUT CURRENT: ±2mA (-0%, +20%).

SWEEP STEP VOLTAGE SELECTIONS: 10mV 20mV 50mV 100mV DC OUTPUT RESISTANCE:  $<10\Omega$ .

### GENERAL

- READING RATES: 41/2 readings per second to one reading every 400 seconds. DATA BUFFER: 450 points maximum.
- GRAPHICAL OUTPUTS: Computer display or Windows supported printers and plotters.
- DIGITAL I/O: Consists of one output, four inputs, +5V (series limited with 33Ω), and COMMON referenced to IEEE-488 COMMON. Output will drive one TTL load. Inputs represent one TTL load.

OPERATING ENVIRONMENT 0° to 40°C, 70% non-condensing RH up to 35°C. STORAGE ENVIRONMENT: -25°C to +65°C.

- WARM-UP: 2 hour to rated accuracy.
- SYSTEM CONFIGURATION: Models 230-1, 590, 595, and 5951 connected as shown in manual.
- MINIMUM COMPUTER CONFIGURATION: 233MHz Pentium, 32MB RAM for Windows 95/98, 64MB RAM for Windows NT 4.0 or 2000, 6GB hard drive, Windows 95, 98, NT 4.0 with Service Pack 6, or 2000, Centronics parallel port.
- RECOMMENDED COMPUTER CONFIGURATION: 350MHz PentiumII, 64MB RAM for Windows 95/98, 128MB RAM for Windows NT 4.0 or 2000, 6GB hard drive, Windows 95, 98, NT 4.0 with Service Pack 6, or 2000, Centronics parallel port.
- Compatible with IEEE-488 cards

National Instruments PCI-GPIB, PCMCIA Keithley KPC-488.2, KPC-488.2AT, KPCI-488

MODEL 82-WIN COMPONENTS:

- 230-1 Programmable Voltage Source
- 595 Quasistatic C-V Meter
- 590 100k/1M C-V Analyzer
- 5909 Calibration Sources for 200pf, 2nF ranges (see Options for additional ranges)
- Remote Input Coupler-Includes Models:
  - 4801 Low noise BNC Cable, 1.2m (4ft), (5 supplied)
    - 7007-1 Shielded IEEE-488 Cable, 1m (3.3 ft), (2 supplied)
  - 7007-2 Shielded IEEE-488 Cable, 2m (6.6 ft), (1 supplied)
  - 7051-2 RG-58C BNC to BNC Cable, 0.6m (2 ft), (3 supplied)

Metrics ICS, version 3.5.2 (includes instrument drivers)

Metrics-ICS-CV Analysis Libraries

SEMICONDUCTOR



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5951